

IN THE CLAIMS

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (Currently Amended) An active matrix display, comprising:  
an array of pixels provided over a common substrate each pixel comprising a display element and a switching device and the array of pixels defining a display area and the pixels being formed from a plurality of thin film ~~layers~~layers;  
column driver circuitry for providing signals to the pixels for driving the display elements; and  
row driver circuitry for providing signals to the pixels for controlling the switching devices of the pixels,  
wherein the display further comprises at least one conductor line extending along an edge of the display over the common substrate and outside the display area, the at least one conductor line comprising at least one layer additional to the plurality of thin film layers defining the array of pixels, and wherein at least one of the row driver circuitry and the column driver circuitry comprises a portion provided on the common substrate outside the display area and which connects to the at least one conductor line

through spurs extending from the at least one conductor line,  
wherein the at least one conductor line extends between the portion  
and the display area.

2. (Currently Amended) ~~A~~-The display as claimed in claim 1,  
wherein the portion of the at least one of the row driver circuitry  
and the column driver circuitry comprises an integrated circuit  
provided on the common substrate outside the display area.

3. (Currently Amended) ~~A~~-The display as claimed in claim 2,  
wherein the row driver circuitry comprises at least one row driver  
integrated circuit mounted on the common substrate and wherein the  
at least one conductor line is parallel to a side edge of the  
display.

4. (Currently Amended) ~~A~~-The display as claimed in claim 3,  
wherein the at least one conductor line comprises a plurality of  
lines parallel to the side edge of the display.

5. (Currently Amended) ~~A~~-The display as claimed in claim 2,  
wherein the column driver circuitry comprises at least one column  
driver integrated circuit mounted on the common substrate, and  
wherein the at least one conductor line is parallel to a top edge  
of the display.

6. (Currently Amended) ~~A~~-The display as claimed in claim 5, wherein the at least one conductor line comprises a plurality of lines parallel to the top edge of the display.

7. (Currently Amended) ~~A~~-The display as claimed in claim 5, wherein the column driver circuitry further comprises a column driver printed circuit board which connects to the at least one column driver integrated circuit.

8. (Currently Amended) ~~A~~-The display as claimed in claim 1, wherein the at least one conductor line comprises a plated line formed over a support defined by one or more of the plurality of thin film layers.

9. (Currently Amended) ~~A~~-The display as claimed in claim 1, wherein the at least one conductor line comprises a printed line.

10. (Currently Amended) ~~A~~-The display as claimed in claim 1, wherein the at least one conductor line comprises a power supply line.

11. (Currently Amended) ~~A~~-The display as claimed in claim 1, wherein each pixel further comprises a storage capacitor connected

between the display element and a common storage capacitor line, and the at least one conductor line comprises the storage capacitor line.

12. (Currently Amended) ~~A~~ The display as claimed in claim 1, wherein the at least one conductor line comprises a clock signal line.

13. (Currently Amended) A method of fabricating an active matrix display, comprising the acts of:

forming an array of pixels over a common substrate within a display area of the substrate, each pixel comprising a display element and a switching device;

subsequently forming at least one conductor line extending along an edge of the display over the common substrate and outside the display area ~~and area; and~~

connecting row driver circuitry or column driver circuitry to the at least one conductor line through spurs extending from the at least one conductor line, wherein at least one of the row driver circuitry and the column driver circuitry comprises a portion provided on the common substrate outside the display area, and wherein the at least one conductor line extends between the portion and the display area.

14.(Currently Amended) ~~A~~The method as claimed in claim 13, wherein the row driver circuitry or the column driver circuitry comprises an integrated circuit and connecting comprises mounting the integrated circuit on the substrate and providing electrical connections to the at least one conductor line.

15.(Currently Amended) ~~A~~The method as claimed in claim 13, wherein the at least one conductor line is formed by plating over one of layers forming the array of pixels.

16.(Currently Amended) ~~A~~The method as claimed in claim 15, wherein the plating is over a thin film metal layer used to form row conductors in the pixel array.

17.(Currently Amended) ~~A display~~The method as claimed in claim 13, wherein the at least one conductor line is formed by printing.

18.(New) The display of claim 1, wherein pixel electrodes of the array of pixels overlap at least one of row and column conductors of at least one of the row driver circuitry and the column driver circuitry so that there is no gap between the row and column conductors and the pixel electrodes.

19.(New) The method of claim 13, wherein pixel electrodes of the array of pixels overlap at least one of row and column conductors of at least one of the row driver circuitry and the column driver circuitry so that there is no gap between the row and column conductors and the pixel electrodes.